



1FW 2633

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Kannan Raj et al.

§

Art Unit: 2633

Serial No.: 09/839,023

§

Examiner: Dalzid E. Singh

Filed: April 20, 2001

§

Docket: ITL.0462US  
P9816

For: Optically Interconnecting  
Multiple Processors

§

Assignee: Intel Corporation

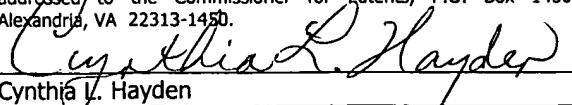
§

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO PAPER NO. 20050330**

Sir:

In response to the office action mailed April 6, 2005, please amend the above-referenced patent application as follows:

|  |
|--|
| Date of Deposit: June 2, 2005  |
| I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. |
|    |
| Cynthia L. Hayden  |